

Design Revision

August 12, 2026

v2.0.0

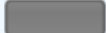


Design Revision 



Design Revision v2.0.0
build 1
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Combined Info	Last Compile 2026-08-12T17:09 Last Emulation 2026-08-12T17:09
Last Compile	2026-08-12T17:09
Last Emulation	2026-08-12T17:09
Design Code	IWJbcQ3Nv0nx

Reset 

Introduction

Design Revision is a simple, helpful utility for automatically tracking and monitoring the revision date of a design file. By placing the Date control in a prominently visible location in the schematic, users can quickly and easily view the most recent emulation and compilation dates and times. A reset button is also provided to clear the block while in emulation mode, making it easy to copy between designs.

The Design Code is stored as a reference so that Core restarts or redundancy state changes do not affect the recorded compilation time. Only new compilations or emulations update the corresponding date and time.



v2.0.0 – August 12, 2026

- Re-released as a plugin
- Added separate Compilation and Emulation fields for more selective UCI usage
- Text fields are now selectable during run time for easy copy and paste
- Reset feature added to clear old data

v1.0.0 – September 1, 2019

Original release